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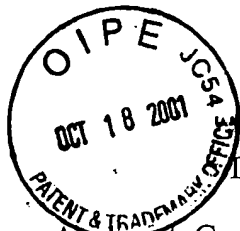
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/032,863
	Filing Date	March 02, 1998
	First Named Inventor	Grigor et al.
	Group Art Unit	2674
	Examiner Name	K. Nguyen
Total Number of Pages in This Submission	Attorney Docket Number	980044(0100.98.1117)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gordon F. Grigor et. al.

Examiner: K. Nguyen

Serial No: 09/032,863

Art Group: 2674

Filing Date: 3/02/98

Docket No: 0100.01117

Title: METHOD AND APPARATUS FOR CONFIGURING MULTIPLE DISPLAYS
ASSOCIATED WITH A COMPUTING SYSTEM

October 15, 2001

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REMARKS

This is in response to a final Office Action, Paper No. 17, mailed August 14, 2001. Claims 24, 29-33 and 38-48 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,874,928 issued to Kou ("Kou") in view of U.S. Patent No. 5,559,525 issued to Zenda ("Zenda"). Furthermore, claims 24, 29-33 and 38-55 stand finally rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,361,078 issued to Caine ("Caine").

Regarding the rejection of claims 24, 29-33 and 38-48 as being unpatentable over Kou in view of Zenda, Applicants respectfully traverse this rejection. Applicants submit that upon closer inspection, it can be seen that the present invention as claimed in claims 24, 29-33 and 38-48 are not obvious in view of the combination of the prior art references.

The Examiner has cited Kou as teaching a video graphics processing circuit, a host computer, a display memory and a frame buffer. Furthermore, the Examiner has indicated Kou teaches a write buffer which is the same buffer as the frame buffer, a read buffer to correspond to the digital storage medium and memory for storing program instructions. The Examiner further indicated that the combination of these elements obviously teaches the claim limitation of "received display preferences regarding at least one of a multiple display as claimed."